

New

TG45AX002 LVDS Output Unit

LVDS Output Unit for "TG45AX Test Signal Generator"

Features

The TG45AX002 LVDS Output Unit generates 12 bits LVDS signal by slotting it into the TG45AX. The output signal is able to generate test signal, natural picture and superimpose signal. It supports variable signal parameter function, too. The dot clock frequency supports 20 MHz to 135 MHz by 1 ch, 40 MHz to 270 MHz by 2 ch and 80 MHz to 400 MHz by 4 ch. The layout of a bit is made of that freely in each GBR channel.

*More than 200MHz dot clock signal output becomes 8 bit.

Specifications

- ◇ Dot clock:
 - 1ch: 20 MHz to 135 MHz, 8/10/12 bit
 - 2ch: 40 MHz to 200 MHz, 8/10/12 bit
201 MHz to 270 MHz, 8 bit
 - 4ch: 80 MHz to 200 MHz, 8/10 bit
201 MHz to 400 MHz, 8 bit
- * The mode change is manually operation.
- ◇ Data format: RGB
- ◇ Output level: LVDS
- ◇ No. of output: 1 system
- ◇ Output connector: 10226-1210PE (3M) MDR26
- ◇ Device: THC63LVD1023 (Thine)
- ◇ VCC voltage level: +5 V or +3.3 V (switchable)



TG45AX002 rear view

MDR26 pin assignment

1	GND	TAOUT -	14
2	TAOUT shield	TAOUT +	15
3	NC	GND	16
4	TBOUT -	TBOUT shield	17
5	TBOUT +	NC	18
6	TCOUT -	TCOUT shield	19
7	TCOUT +	TEOUT -	20
8	TEOUT shield	TEOUT +	21
9	GND	TCLK -	22
10	TCLK shield	TCLK +	23
11	VCC	VCC	24
12	TDOUT -	TDOUT shield	25
13	TDOUT +	GND	26

Mode and output specifications

Mode	Output 1	Output 2	Output 3	Output 4
1ch	8bit	8 bit	Same as Output 1	Same as Output 1
	10bit	10 bit	Same as Output 1	Same as Output 1
	12 bit	8 bit (upper)	4 bit (lower)	Same as Output 1
2ch	8 bit	PHASE0 8 bit	PHASE1 8 bit	Same as Output 2
	10 bit	PHASE0 10 bit	PHASE1 10 bit	Same as Output 2
	12 bit	PHASE0 8 bit (Upper)	PHASE0 4 bit (lower)	PHASE1 8 bit (upper)
4ch	8 bit	PHASE0 8 bit	PHASE1 8 bit	PHASE2 8 bit
	10 bit	PHASE0 10 bit	PHASE1 10 bit	PHASE3 10 bit

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